

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Yasuyuki MORISHITA  
Title: SEMICONDUCTOR DEVICE  
Appl. No.: 09/621,614  
Filing Date: 07/21/00  
Examiner: Nadav, Ori  
Art Unit: 2811



RECEIVED  
FEB-4 2002  
TC 2800 MAIL ROOM

**LETTER TO THE OFFICIAL DRAFTSMAN**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

Please amend the drawings of Figures 6, 7, 9-11, and 13-15 as indicated on the attached red-marked copies.

Respectfully submitted,

Date: December 18, 2001

By David A. Blumenthal

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Fig. 5

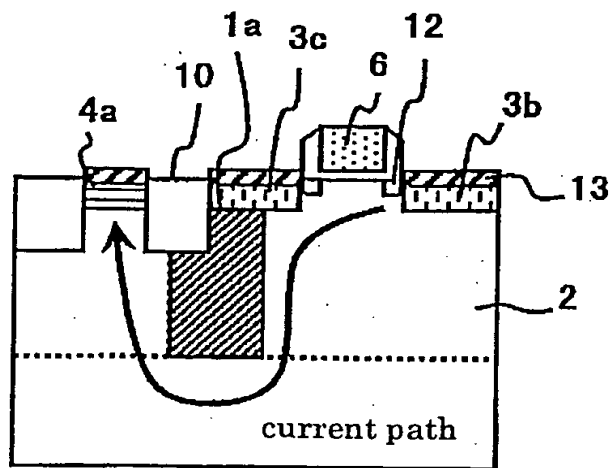
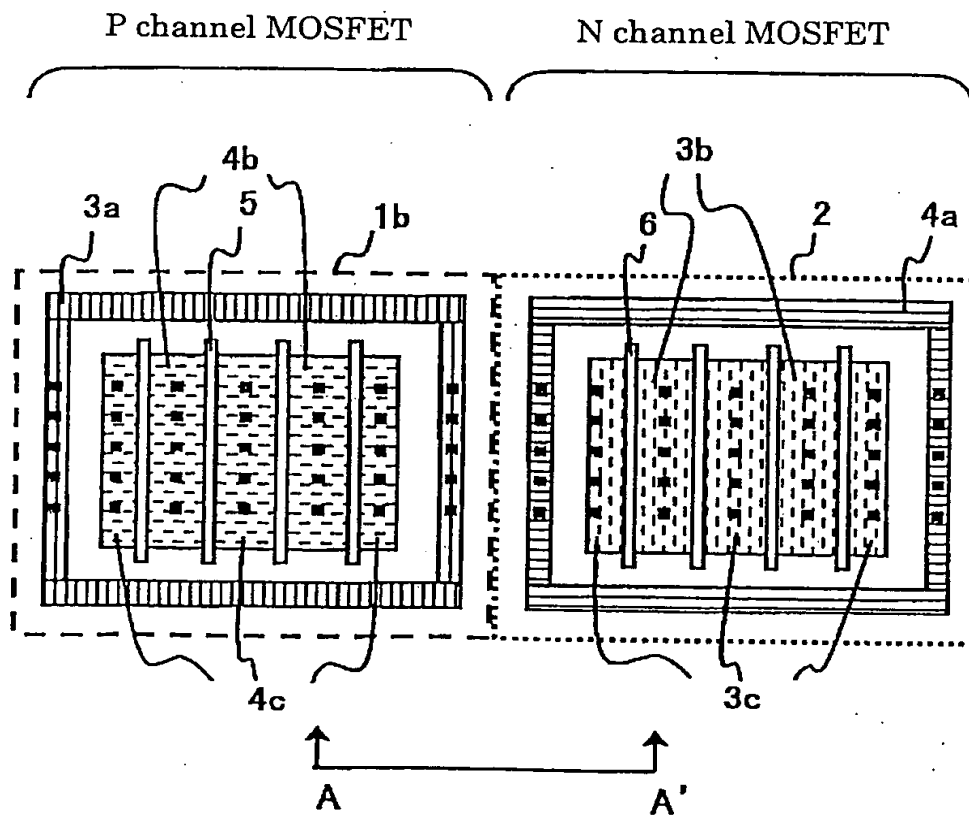


Fig. 6

PRIOR ART



PRIOR ART



## N channel MOSFET



## N channel MOSFET

Fig. 9

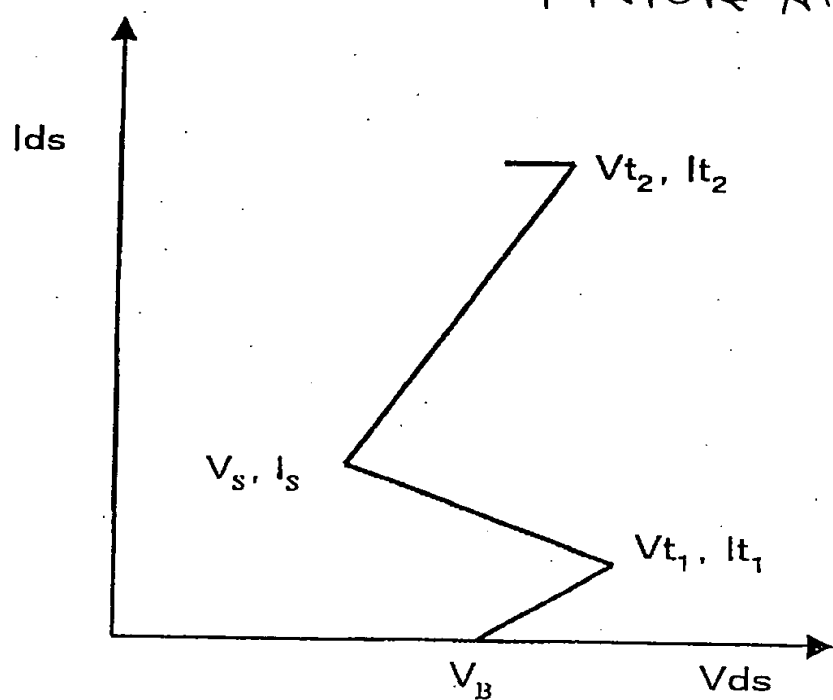


Fig. 10

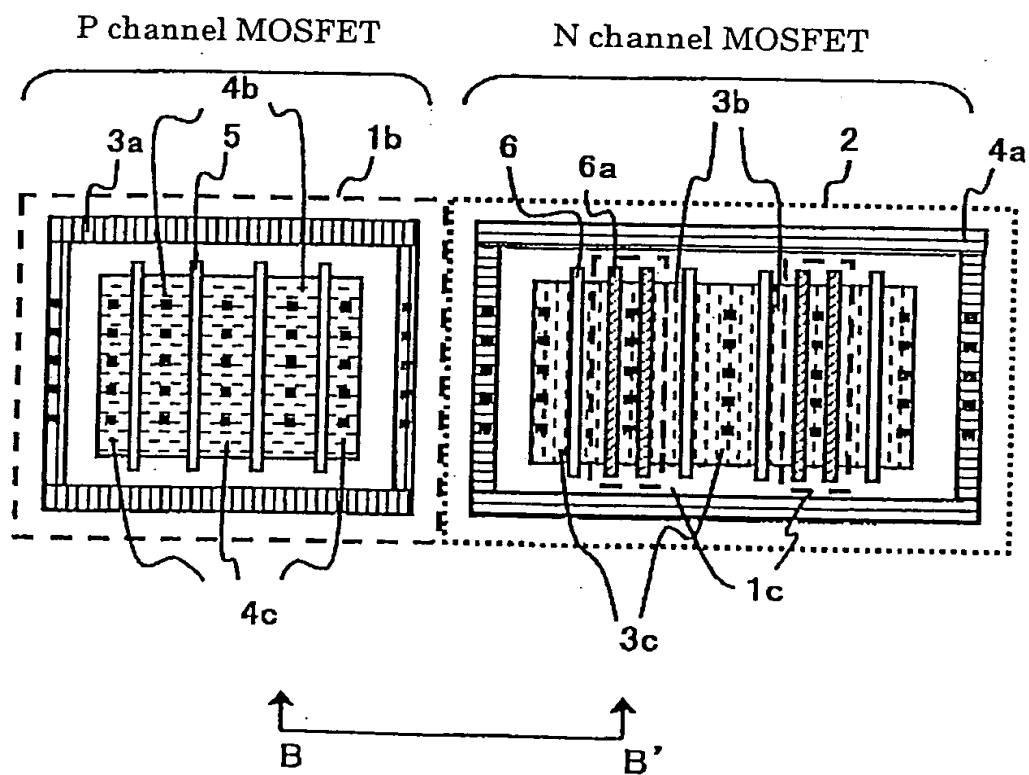


Fig. 11

PRIOR ART

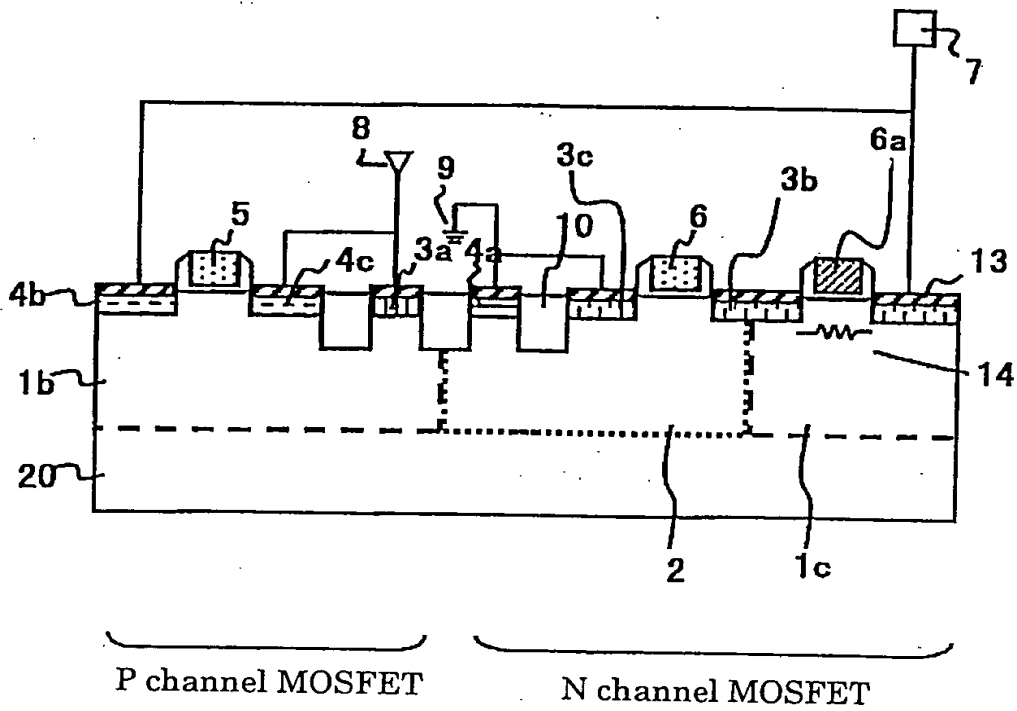


Fig. 12

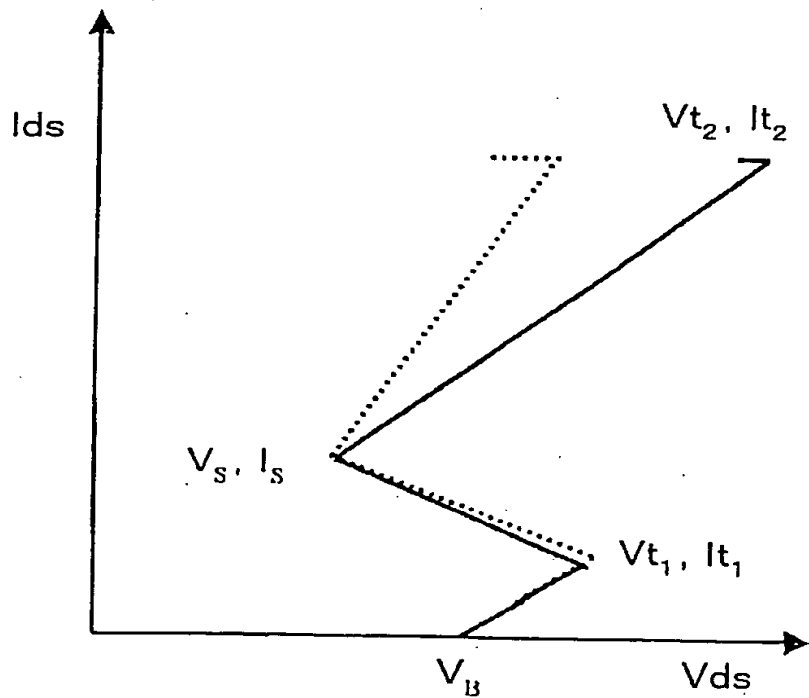


Fig. 13

PRIOR ART

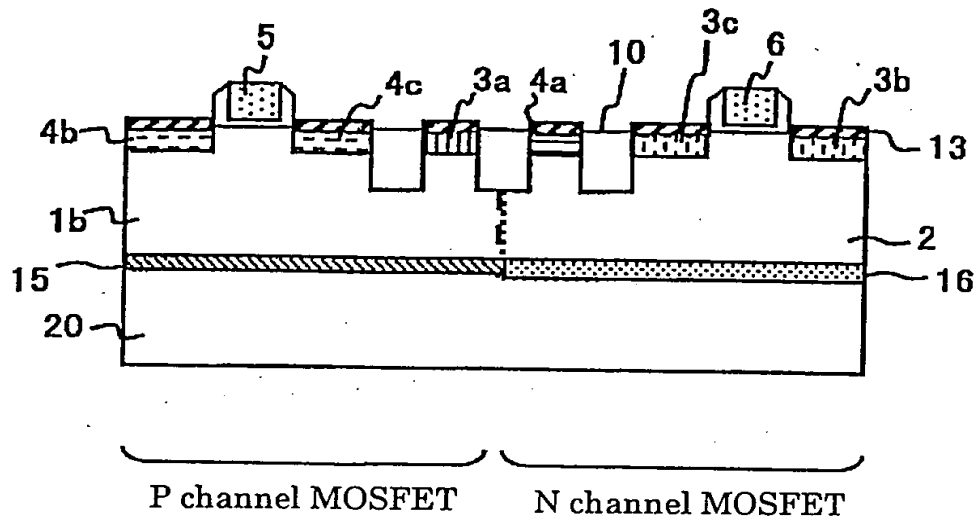


Fig. 14

PRIOR ART

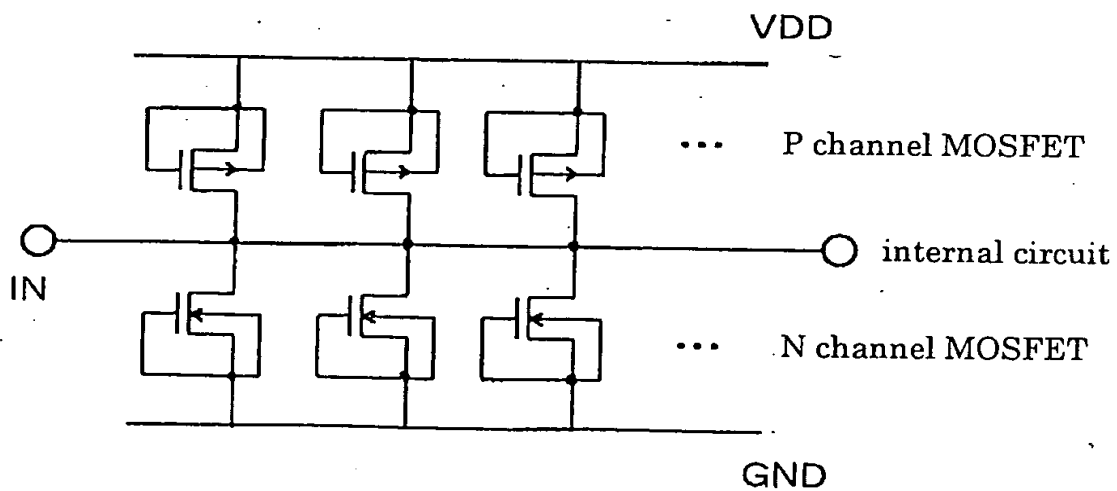


Fig. 15

PRIOR ART

